INDIAN INSTITUTE OF TECHNOLOGY BHUBANESWAR



Research and Development (R&D)

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Advt. No.: IITBBS/R&D/RP252/Rect/38/2021-22 Date: 06.12.2021

Applications are invited on prescribed format for the following assignment in a purely time bound research project undertaken in the School of Electrical Sciences of this Institute.

1. Name of the Temporary : Junior Research Fellow (JRF) – 1 Post

Assignment

2. Name of the temporary research : "High-speed and energy efficient CMOS transceiver design for

Project full-duplex chip-to-chip serial link"

3. Name of the Sponsoring Agency : Science and Engineering Research Board (SERB), Department of

Science and Technology (DST)

4. Consolidated Emolument : Rs. 31,000/- + HRA @16% per month

5. Qualification : (a) B.E/ B.Tech degree in Electronics/ Electronics and

Communication Engineering with two years of experience, or

M.Tech degree in VLSI circuits/ allied discipline

(b) The applicant should be able to demonstrate skills in CMOS based analog circuit design and should have experience in

circuit design, post-layout simulation using tools such as

Cadence Virtuoso.

6. Period of Engagement : Initially for 6 months with possible extension depending on

good performance

7. Duration of the project : 2 years

Interested eligible persons may apply through online application system on or before 24th December 2021. Web link https://webapps.iitbbs.ac.in/sric-application/

To complete the registration process, please visit the Institute's website: www.iitbbs.ac.in. After entering the website, go to the 'Jobs' tab at the top then select the option 'Recruitment for Research Project Work' and fill up the online application form after completion of registration process.

Selection to the posts will be done through online interaction only from among the shortlisted candidates. There is no need to send hard copy of the application. The shortlisted candidates will be intimated through email only for online Interaction with the designated Committee through skype/google meet etc.

Sd/-

Asst. Registrar (R&D)